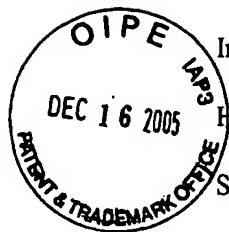


## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



In re patent application of : Assignee: Intel Corporation  
 )  
 HAZUCHA, P. et al. : Customer No: 34610  
 ) Confirmation No. 8425  
 Serial No. 10/748,298 : Examiner: Nguyen, L.  
 )  
 Filed: December 31, 2003 : Group Art Unit: 2816  
 )  
 For: TIMING CIRCUIT FOR SEPARATE POSITIVE AND NEGATIVE EDGE  
 PLACEMENT IN A SWITCHING DC-DC CONVERTER

**AMENDMENT UNDER 37 CFR § 1.116**

U.S. Patent and Trademark Office  
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December 16, 2005

*OK to enter*

Sir:

The following amendments and remarks are respectfully submitted in response to the  
 Final Office Action mailed on October 27, 2005, in connection with the above-identified  
 application.

*ln ✓**o/12/06*

**Amendments to the Claims** are in the Listing of Claims beginning on page 2.

**Remarks** begin on page 15.

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